

CLAIMS

1. (Amended) An information controlling apparatus comprising:

frequency information inputting means for inputting frequency information about a synchronizing clock signal having a clock frequency which is variable;

time parameter storing means for storing a time parameter needed by an information processing section to operate for information processing;

comparing means for comparing a time period based on a first clock frequency input through said frequency information inputting means, with the time stored in said parameter storing means;

time setting means for setting a time period based on a second clock frequency replacing said time period based on said first clock frequency if said time period is not found to meet said time designated by said time parameter upon comparison by said comparing means; and

operation controlling means which is supplied with said synchronizing clock signal as an operating clock signal and which operates to control said information processing section in suitably timed relation with the time period calculated from said clock frequency.

2. (Deleted)

3. (Amended) The information processing apparatus according to claim 1, wherein the frequency of said synchronizing clock signal is constituted by either a currently or a subsequently effective frequency.

4. (Amended) The information processing apparatus according to claim 1, wherein said frequency information inputting means inputs said frequency information in encoded form, and wherein said time setting means and operation controlling means decode said frequency information in encoded form.

5. (Amended) The information processing apparatus according to claim 1, wherein said time setting means sets said time period by adding to said frequency information either a cycle value of said clock frequency or a value proportional to said cycle value.

6. (Amended) An information storing apparatus comprising:

frequency information inputting means for inputting frequency information about a synchronizing clock signal having a clock frequency which is variable;

time parameter storing means for storing a time parameter needed by an information processing section to operate for information processing;

comparing means for comparing a time period based on a first clock frequency input through said frequency information inputting means, with the time stored in said parameter storing means;

time setting means for setting a time period based on a second clock frequency replacing said time period based on said first clock frequency if said time period is not found to meet said time designated by said time parameter upon comparison by said comparing means; and

information storing means which is supplied with said synchronizing clock signal as an operating clock signal and which performs an information storing operation in suitably timed relation with the time period calculated from said clock frequency.

7. (Amended) The information storing apparatus according to claim 6, wherein said information storing means has a plurality of memory cells for storing data by accumulating electrical charges, and wherein said information storing operation includes at least one of the operations comprising bringing electrical charges from part of said memory cells to an amplifier, inputting and outputting electrical charges to and from said amplifier, and causing said amplifier to accumulate electrical charges in said memory cells.

8. (Amended) The information storing apparatus according to claim 7, wherein said memory cells are formed in a memory body, and wherein said frequency information operating means is formed in memory control section for controlling said memory body.

9. (Amended) The information storing apparatus according to claim 6, wherein the calculation on said frequency information is carried out by decoding said frequency information in encoded form.

10. (Amended) The information storing apparatus according to claim 6, wherein the calculation on said frequency information is carried out by adding a corresponding signal cycle to the frequency in said frequency information.

11. (Amended) An information storing apparatus comprising:

frequency controlling means for generating frequency information about a synchronizing clock signal having a clock frequency which is variable;

frequency information inputting means for inputting said frequency information;

time parameter storing means for storing a time parameter needed by an information processing section to operate for information processing;

comparing means for comparing a time period based on a first clock frequency input through said frequency information inputting means, with the time designated by said time parameter stored in said parameter storing means;

time setting means for setting a time period based on a second clock frequency replacing said time period based on said first clock frequency if said time period is not found to meet said time designated by said time parameter upon comparison by said comparing means; and

information storing means which is supplied with said synchronizing clock signal as an operating clock signal and which performs an information storing operation in suitably timed relation with the time period calculated from said clock frequency.

12. (Deleted)

13. (Amended) An information processing method comprising the steps of:

inputting frequency information about a synchronizing clock signal having a clock frequency which is variable;

storing a time parameter needed to execute a control operation in carrying out information processing;

comparing a time period based on a first clock

frequency that has been input, with the time designated by said time parameter stored in said parameter storing means;

setting a time period based on a second clock frequency replacing said time period based on said first clock frequency if said time period is not found to meet said time designated by said time parameter upon comparison; and

performing said information processing in suitably timed relation with the time period calculated from said clock frequency given said synchronizing clock signal as an operating clock signal.

14. The information processing method according to claim 13, wherein said information processing includes storing information into a plurality of memory cells capable of holding data through accumulation of electrical charges, said information processing further including at least one of the operations comprising bringing electrical charges from part of said memory cells to an amplifier, inputting and outputting electrical charges to and from said amplifier, and causing said amplifier to accumulate electrical charges in said memory cells.

15. (Amended) An information processing program

comprising the steps of:

processing frequency information about a synchronizing clock signal having a clock frequency which is variable;

storing a time parameter needed to execute a control operation in carrying out information processing;

comparing a time period based on a first clock frequency that has been input, with the time designated by said time parameter stored in said parameter storing means;

setting a time period based on a second clock frequency replacing said time period based on said first clock frequency if said time period is not found to meet said time designated by said time parameter upon comparison; and

supplying said synchronizing clock signal as an operating clock signal to an information processing section for performing information processing while operating to control said information processing section in suitably timed relation with the time period calculated from said clock frequency.

16. The information processing program according to claim 13, wherein said information processing includes storing information into a plurality of memory cells

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capable of holding data through accumulation of electrical charges, said information processing further including at least one of the operations comprising bringing electrical charges from part of said memory cells to an amplifier, inputting and outputting electrical charges to and from said amplifier, and causing said amplifier to accumulate electrical charges in said memory cells.